



17-20 October 2019 | Kochi, Kerala

## Special Session on Advanced On-chip Packaging and Systems

Call for Paper

### Special Session Track on

#### Advanced On-chip Packaging and Systems

- 3D-ICs/TSVs/Interposers
- Testing on 3D-IC and SiP
- Signal and Thermal Integrity
- Power Integrity/Power Distribution Networks (PDNs) /Ground Noise
- Computational Electromagnetics and Multi-physics Methods for SI/PI/TI Analysis
- Thermal Management Design for 3D-ICs and SiP
- Design and Modeling for High-speed Channels and Interconnects
- High speed serial links jitter budgeting
- Jitter segregation algorithms and tools
- Time / Frequency Domain Measurement Techniques
- Power supply induced jitter and transfer functions.
- Nanoelectronics for 3D-ICs and SiP.
- Others

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*Authors are requested to submit papers for this special session (upto max. 6 pages). The paper should be submitted in the two column IEEE format. Submission can be done through TENCON website at*

[www.tencon2019.org/special\\_sessions.html](http://www.tencon2019.org/special_sessions.html)

**Please Note: During submission, this Special Session has to be selected.**

#### Special Session Track Chair

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#### IMPORTANT DATES

#### Papers Submission

Submission Deadline : June 30, 2019

Paper Acceptance Date : August 15, 2019

Camera-Ready : August 30, 2019

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